

Category	Node	Process Platform	Process Description	Booking Deadline	2022/1H						2022/2H						
					Jan	Feb	Mar	Apr	May	Jun	July	Aug	Sep	Oct	Nov	Dec	
Fab1	95nm	95nm EE/OTP/MTP	1.5/5V & SG 5V	5th/ Month				◆					◆				
	0.18µm BCD	BD180GE0G	BCD Ehnanced_Generic_5V CMOS, Analog LDMOS(VGS 5V , VDS 24V/40V), SNLDMOS (VGS 5V , VDS 12V/18V/20V/24V/30V/40V)			◆		◆			◆				◆		
					BD180GEZ8	BCD Generic_Ehnanced_1.8V/5V CMOS ,Analog LDMOS (VGS 5V, VDS 24V/40V), SNLDMOS (VGS 5V , VDS 12V/18V/20V/24V/30V/40V)											
	0.35µm BCD	CD350G-40V(PMU350)	CDMOS_Generic_3.3V/5V(Vgs) 12V/18V/30V/40V(Vds)					◆							◆		
		BCD350GE	BCD_Ehnanced_5V/24V(Vgs) 12V/24V/30V/40V(Vds)														
Fab3	0.13/0.11µm	Logic/Analog(MS/RF)/Emb Flash/BCD	1.5V, 3.3V/HV/BCD	5th/Month	◆		◆		◆		◆		◆		◆		
Fab7	90nm BCD	90nm Low Power BCD	1.5V/5V/5-40V(BCD)	20th/ Month		◆				◆				◆			
	90nm Eflash	90nm Embedded Nord-Flash	Aggressive 90nm LP , ULL 1.5V/5V(Nord-Flash)					◆			◆				◆		
	65nm/55nm	55nm Logic /55nm Nord-Flash	55nm 1.2V/2.5V(Logic) ,55nm 1.2V/5V or 1.2V/2.5V/5V(Nord-Flash)					◆				◆				◆	

Schedule of MPW Shuttle

-MPW booking form show all deadline date;

Note

-MPW schedule is subjected to change depending on participation rate

-Additional & special projects MPW could be opened if necessary

MPW cycle time forecast

-Data-in to JDV release (assuming data clean):

For Fab1 and Fab3 0.18 : 8 working days

For Fab3/7 0.13um,90nm or 65nm/55nm process (chips <= 10): 10 working days

-Wafer Start to Wafer out: According to process requirement with Hot rank

-Wafer out to die saw sample starting ship: 7D