

2022 Shuttle Chip™ Schedule

The opportunity to verify an initial chip design in silicon with lower cost

No : Tape-out Start Date

Process	Tech	Fab	2022											
			1Q			2Q			3Q			4Q		
			Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Analog /BCD (AN07/AN180/HP180 BD180LV/BD180MV BD180XH/BD180XA)	0.18um	Fab1	26	23	30	27	25	29	27	31	28	26	30	28
	BCD (BEOL 0.15um BD180LVA)		0.15um	12		16		11		13		14		16
Analog /BCD/ SOI (BD130/RS13)	0.13um	Fab2		16			11			31				30
	Mixed Signal/ RF HRS		0.11um		23		13			13			12	
CIS			0.11um				27						26	